

NOT RECOMMENDED FOR NEW DESIGN

DMN3005LK3



N-CHANNEL ENHANCEMENT MODE MOSFET

Features

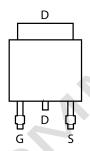
- Low On-Resistance
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

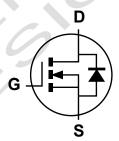
- Case: TO252
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Matte Tin Finish annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208 (a)
- Weight: 0.33 grams (approximate)







Pin Out -Top View



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3005LK3-13	TO252	2500 / Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http"//www.diodes.com/products/packages.html.

Marking Information



⊃¹¹ = Manufacturer's Marking
 N3005L = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Year (ex: 09 = 2009)
 WW = Week (01 - 53)





DMN3005LK3

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +85°C	ID	14.5 10.5	Α
Continuous Drain Current (Note6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +85°C	I _D	22 16	Α
Pulsed Drain Current (Note 7)			I _{DM}	48	Α

Thermal Characteristics

		~	
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	1.68	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	Reja	74.3	°C/W
Power Dissipation (Note 6)	P _D	4.1	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 6)	ReJA	30.8	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

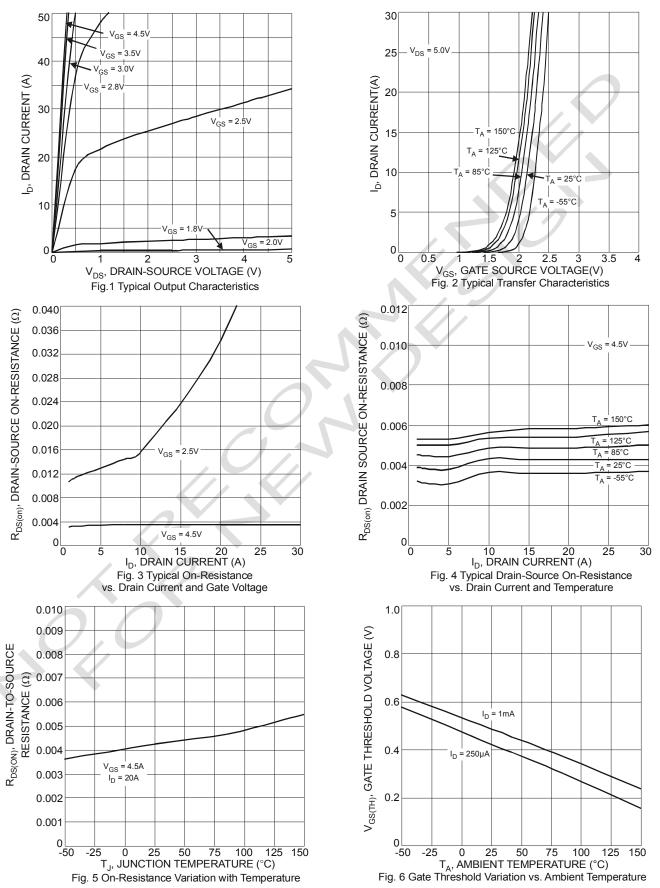
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						•
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current T _J = 25°C	I _{DSS}	-	-	1.0	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	1.0	1.5	2.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
Static Drain-Source On-Resistance	R _{DS(ON)}	-	2.5 3.8	4.5 6.5	mΩ	$V_{GS} = 10V, I_D = 20A$ $V_{GS} = 4.5V, I_D = 20A$
Forward Transfer Admittance	Y _{fs}	-	22	-	S	V _{DS} = 15V, I _D = 15A
Diode Forward Voltage	V _{SD}	-	8.0	1.0	V	V _{GS} = 0V, I _S = 20A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	-	4342		pF	15)()(
Output Capacitance	Coss	-	1801	-	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	-	669	-	pF	1 - 1.000112
Gate Resistance	R_{g}	-	1.76	-	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Q_{g}	-	46.9	-	nC	1, 1,5)()(
Gate-Source Charge	Q_{gs}	-	14.3	-	nC	$V_{GS} = 4.5V, V_{DS} = 15V,$ $I_{D} = 15A$
Gate-Drain Charge	Q_{gd}	-	18.6	-	nC	ID - 19A
Turn-On Delay Time	t _{D(on)}	-	7.9	-	ns	
Turn-On Rise Time	t _r	-	22.8	-	ns	V _{DS} = 15V, V _{GS} = 10V,
Turn-Off Delay Time	t _{D(off)}	-	73.4	-	ns	$R_L = 1.3\Omega R_G = 3\Omega$
Turn-Off Fall Time	t _f	-	43.5	-	ns	1
Body Diode Reverse Recovery Time	t _{rr}	-	23.5	-	ns	I _F = 20A, dl/dt = 100A/μs
Body Diode Reverse Recovery Charge	Qrr	-	15.6	-	nC	I _F = 20A, dl/dt = 100A/μs

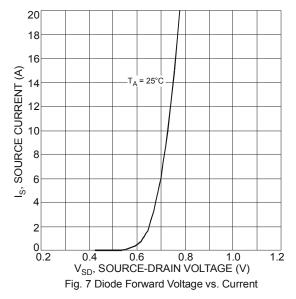
Notes:

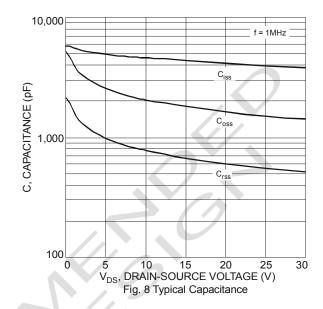
- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout, single sided.
- Device mounted on 2" x 2" FR-4 PCB with high coverage 2oz. copper, single sided.
 Repetitive rating, pulse width limited by junction temperature and current limited by package.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.











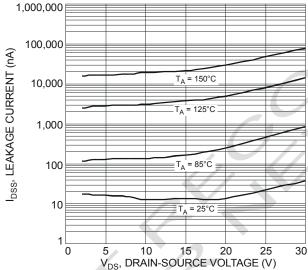
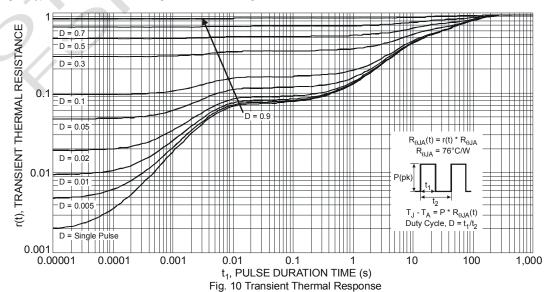


Fig. 9 Typical Drain-Source Leakage Current vs Voltage

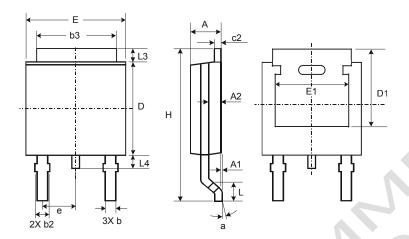






Package Outline Dimensions

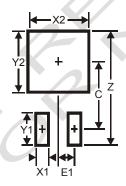
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252						
Dim	Min	Max	Тур			
A	2.19	2.39	2.29			
A1	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
c2	0.45	0.58	0.531			
۵	6.00	6.20	6.10			
D1	5.21	-	_			
е			2.286			
E	6.45	6.70	6.58			
E1	4.32	_	-			
Н	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°	_			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
С	6.9
E1	2.3



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DMN3005LK3

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